



N- and P-Channel 1.8-V (G-S) MOSFET

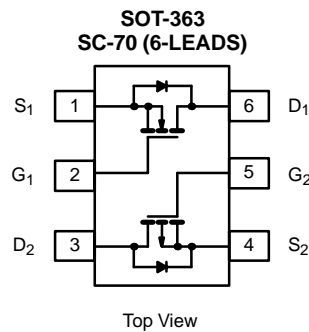
PRODUCT SUMMARY			
	V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
N-Channel	12	0.235 @ V _{GS} = 4.5 V	1.3
		0.280 @ V _{GS} = 2.5 V	1.2
		0.340 @ V _{GS} = 1.8 V	1.0
P-Channel	-12	0.535 @ V _{GS} = -4.5 V	-0.86
		0.880 @ V _{GS} = -2.5 V	-0.67
		1.26 @ V _{GS} = -1.8 V	-0.56

FEATURES

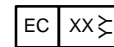
- TrenchFET® Power MOSFETs
- Thermally Enhanced SC-70 Package
- Fast Switching to Minimize Gate and Switching Losses

APPLICATIONS

- Baseband DC/DC Converter Switch for Portable Electronics



Marking Code



Lot Traceability and Date Code

Part # Code

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)							
Parameter	Symbol	N-Channel		P-Channel		Unit	
		5 secs	Steady State	5 secs	Steady State		
Drain-Source Voltage	V _{DS}	12		-12		V	
Gate-Source Voltage	V _{GS}	±8					
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25 °C	1.3	1.2	-0.86	-0.77	A	
	T _A = 85 °C	0.9	0.8	-0.62	-0.55		
Pulsed Drain Current	I _{DM}	3		-2			
Continuous Source Current (Diode Conduction) ^a	I _S	0.5	0.39	-0.5	-0.39		
Maximum Power Dissipation ^a	T _A = 25 °C	0.6	0.47	0.6	0.47	W	
	T _A = 85 °C	0.3	0.25	0.3	0.25		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150				°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 5 sec	R _{thJA}	170	210	°C/W
	Steady State		220	265	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	105	125	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Static							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 100 μA	N-Ch	0.45		1	V
		V _{DS} = V _{GS} , I _D = -100 μA	P-Ch	-0.45		1	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V	N-Ch			±100	nA
			P-Ch			±100	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 9.6 V, V _{GS} = 0 V	N-Ch			1	μA
		V _{DS} = -9.6 V, V _{GS} = 0 V	P-Ch			-1	
		V _{DS} = 9.6 V, V _{GS} = 0 V, T _J = 85 °C	N-Ch			5	
		V _{DS} = -9.6 V, V _{GS} = 0 V, T _J = 85 °C	P-Ch			-5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	N-Ch	3			A
		V _{DS} ≤ -5 V, V _{GS} = -4.5 V	P-Ch	-2			
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 1.2 A	N-Ch		0.195	0.235	Ω
		V _{GS} = -4.5 V, I _D = -0.77 A	P-Ch		0.445	0.535	
		V _{GS} = 2.5 V, I _D = 1.0 A	N-Ch		0.230	0.280	
		V _{GS} = -2.5 V, I _D = -0.6 A	P-Ch		0.735	0.880	
		V _{GS} = 1.8 V, I _D = 0.2 A	N-Ch		0.284	0.340	
		V _{GS} = -1.8 V, I _D = -0.2 A	P-Ch		1.05	1.26	
Forward Transconductance ^a	g _{fs}	V _{DS} = 5 V, I _D = 1.2 A	N-Ch		0.8		S
		V _{DS} = -5 V, I _D = -0.77 A	P-Ch		1.2		
Diode Forward Voltage ^a	V _{SD}	I _S = 0.39 A, V _{GS} = 0 V	N-Ch		0.8	1.2	V
		I _S = -0.39 A, V _{GS} = 0 V	P-Ch		-0.8	-1.2	
Dynamic^b							
Total Gate Charge	Q _g	N-Channel V _{DS} = 6 V, V _{GS} = 4.5 V, I _D = 1.2 A P-Channel V _{DS} = -6 V, V _{GS} = -4.5 V, I _D = -0.1 A	N-Ch		0.8	1.2	nC
			P-Ch		1.1	1.8	
Gate-Source Charge	Q _{gs}		N-Ch		0.15		
Gate-Drain Charge	Q _{gd}		P-Ch		0.3		
			N-Ch		0.20		
			P-Ch		0.25		
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 6 V, R _L = 12 Ω I _D ≅ 0.5 A, V _{GEN} = 4.5 V, R _G = 6 Ω P-Channel V _{DD} = -6 V, R _L = 12 Ω I _D ≅ -0.5 A, V _{GEN} = -4.5 V, R _G = 6 Ω	N-Ch		15	25	ns
			P-Ch		17	25	
Rise Time	t _r		N-Ch		25	40	
			P-Ch		30	45	
Turn-Off Delay Time	t _{d(off)}		N-Ch		25	40	
			P-Ch		15	25	
Fall Time	t _f		N-Ch		10	15	
			P-Ch		10	15	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 0.39 A, di/dt = 100 A/μs	N-Ch		20	40	
		I _F = -0.39 A, di/dt = 100 A/μs	P-Ch		25	40	

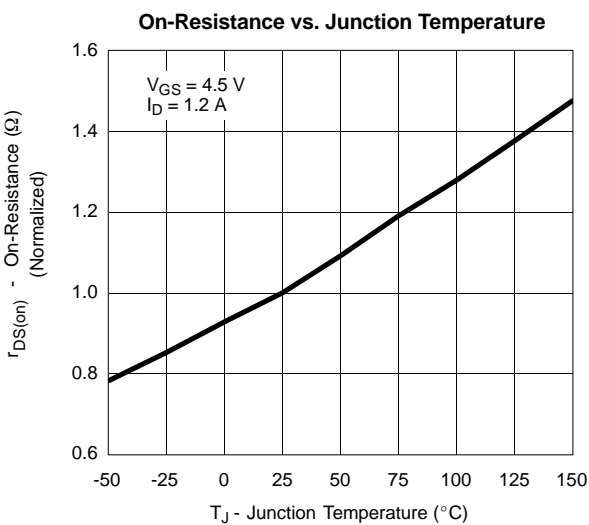
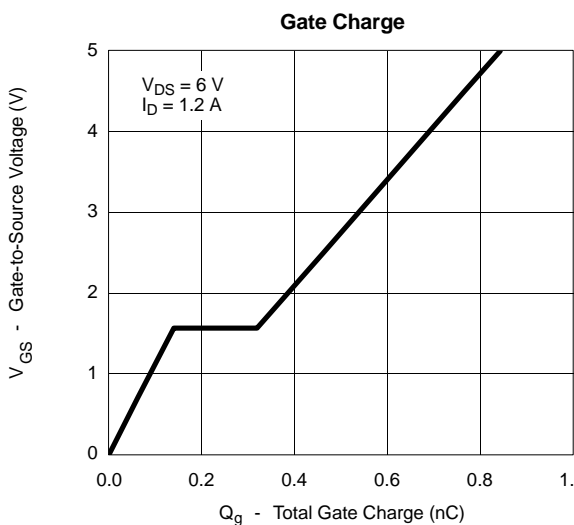
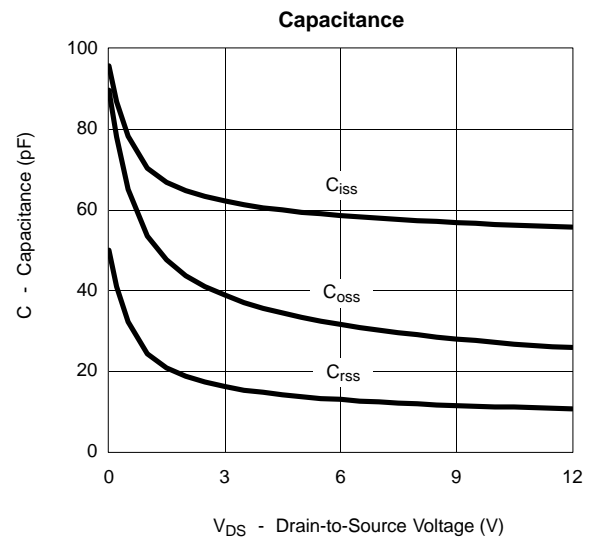
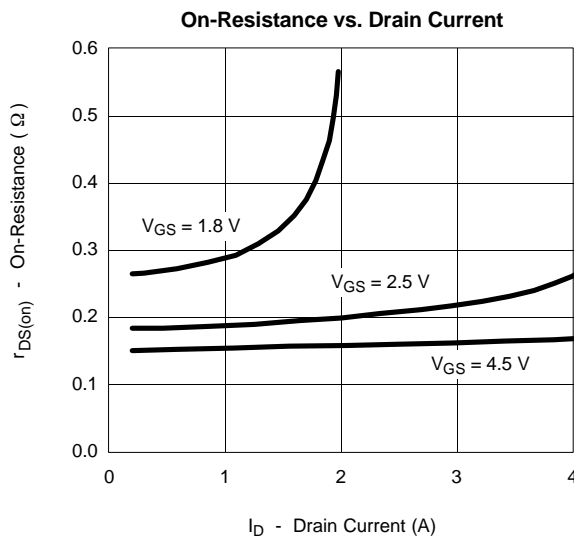
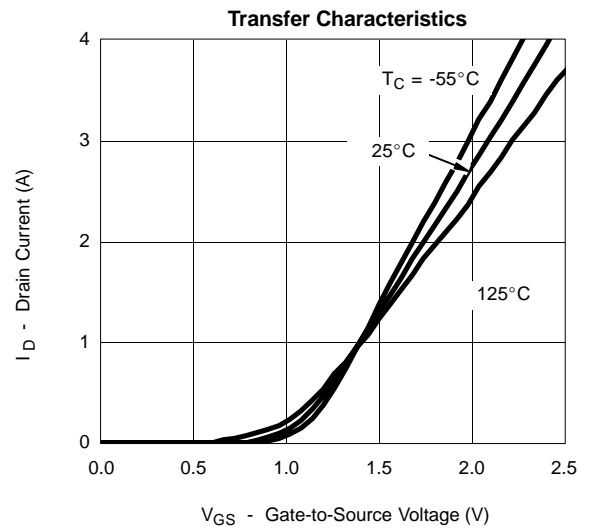
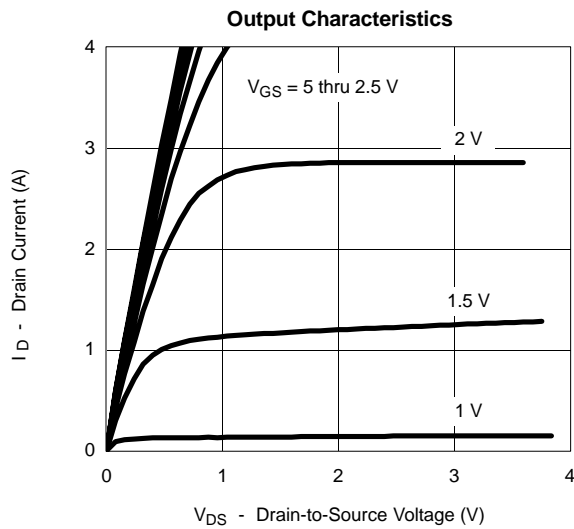
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

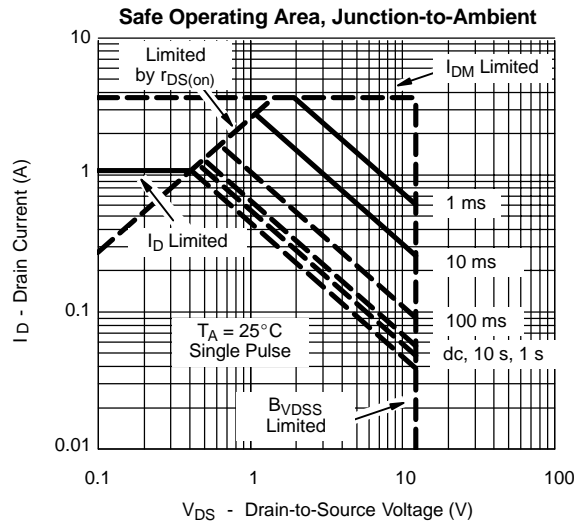
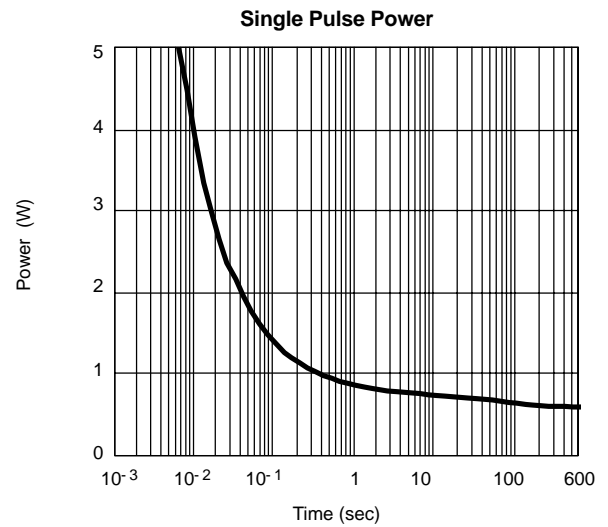
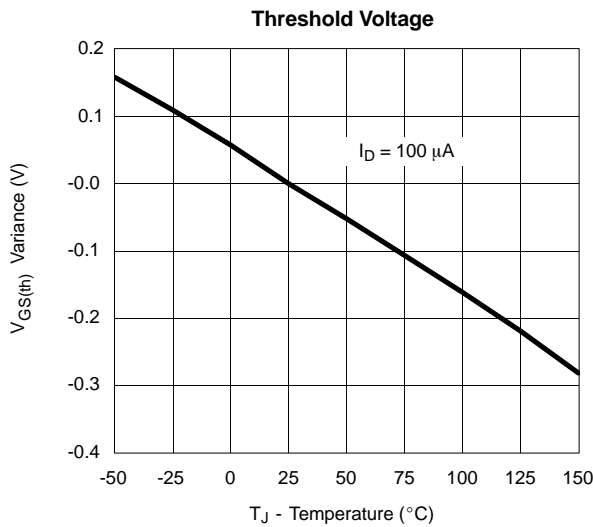
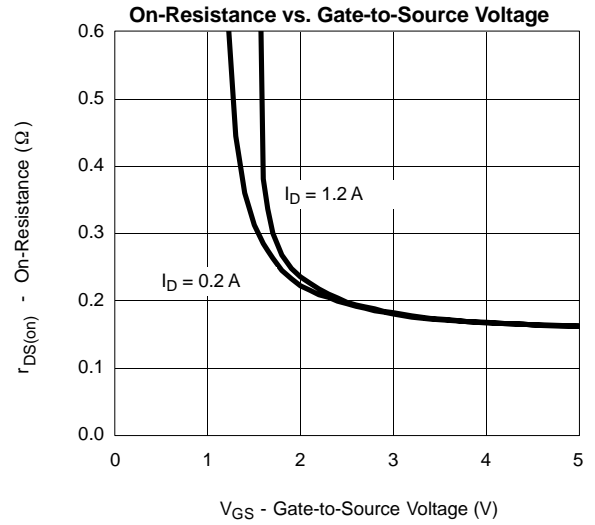
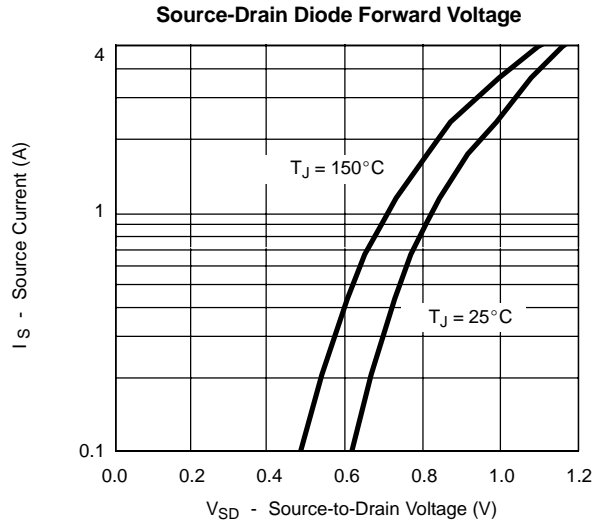
N-CHANNEL





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N-CHANNEL

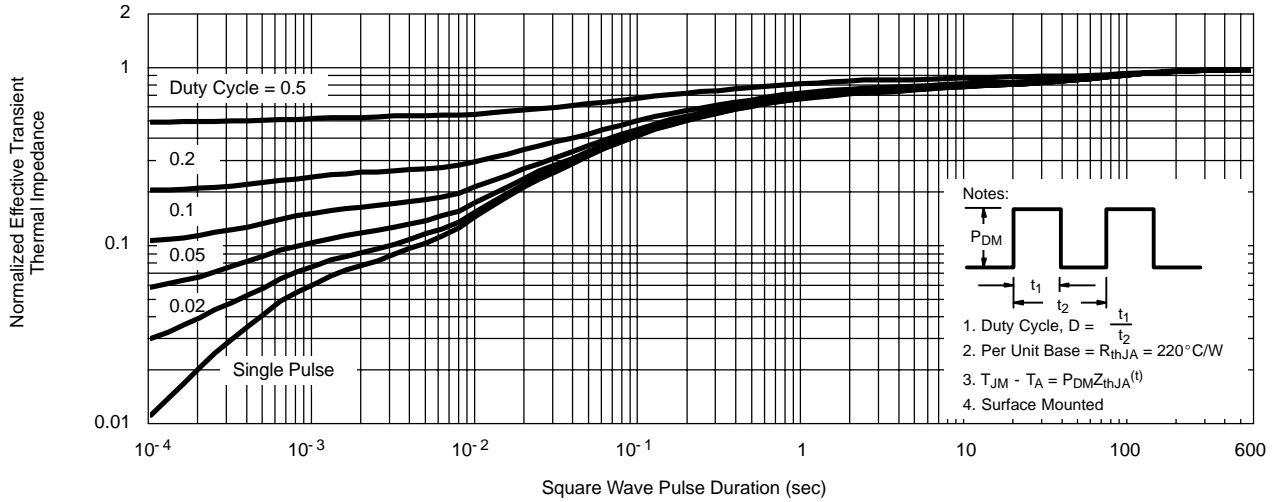




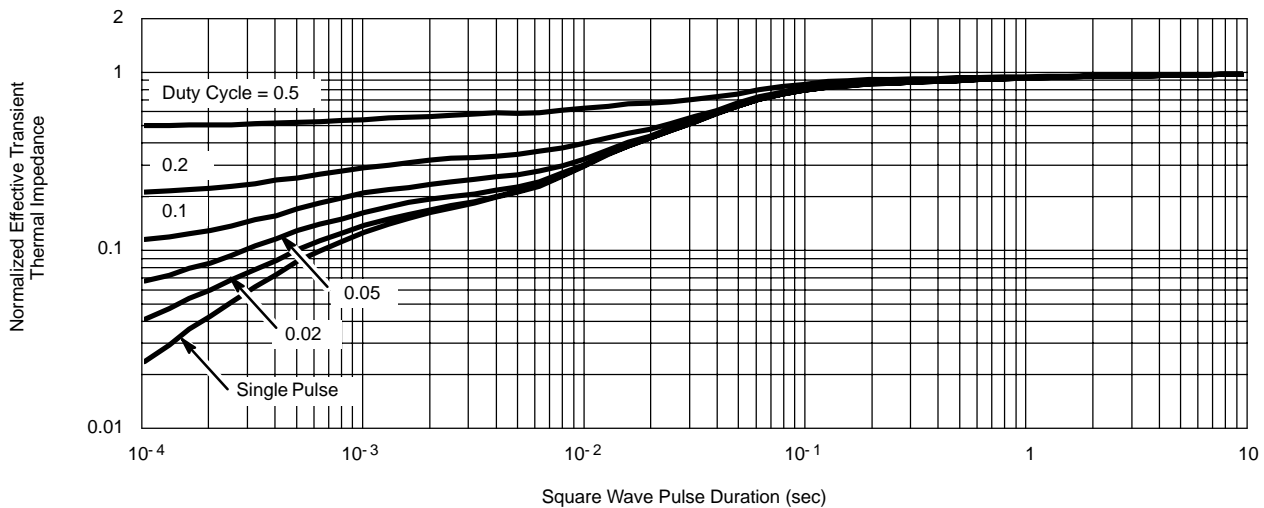
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

N-CHANNEL

Normalized Thermal Transient Impedance, Junction-to-Ambient



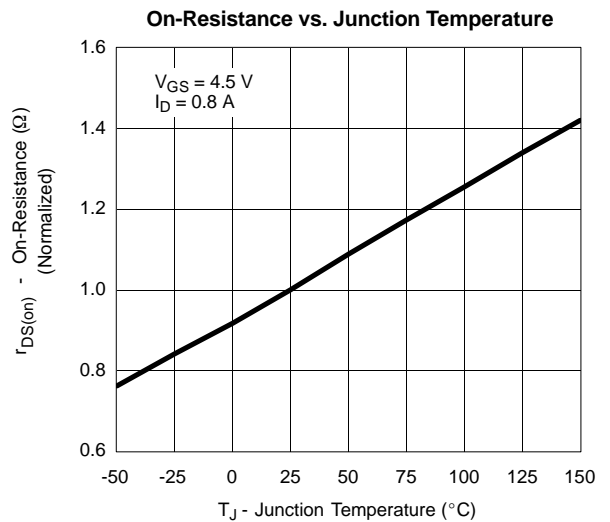
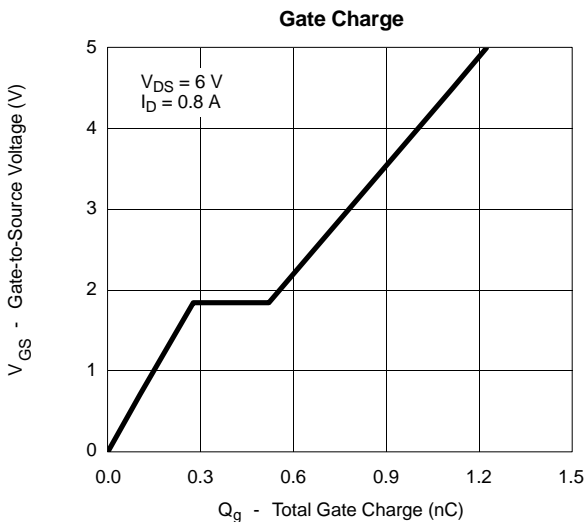
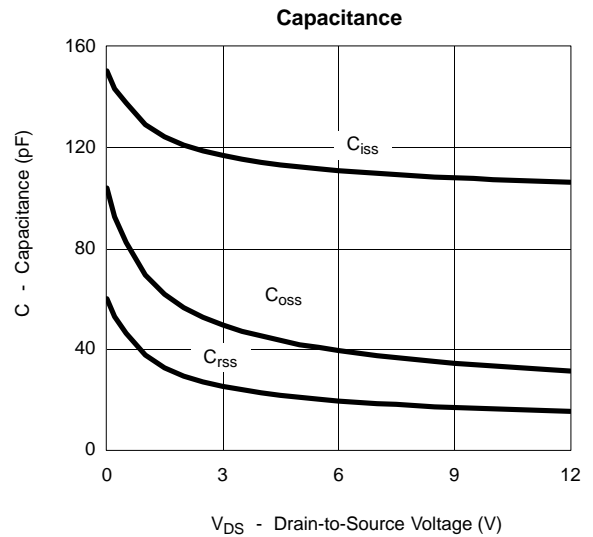
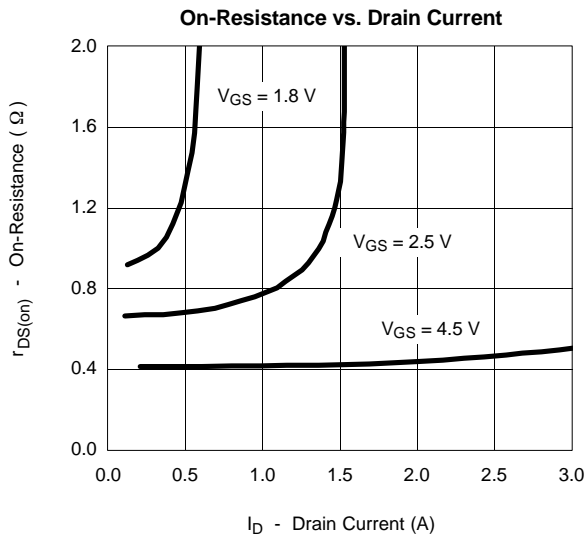
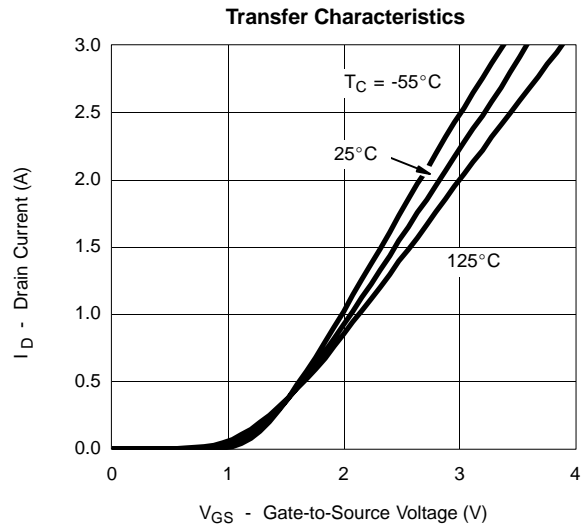
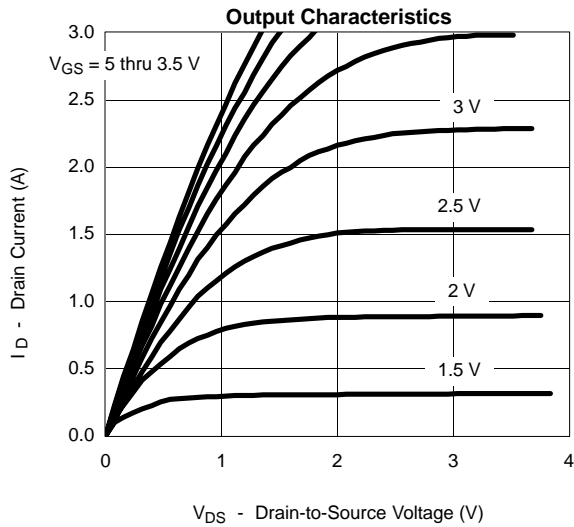
Normalized Thermal Transient Impedance, Junction-to-Foot





TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

P-CHANNEL

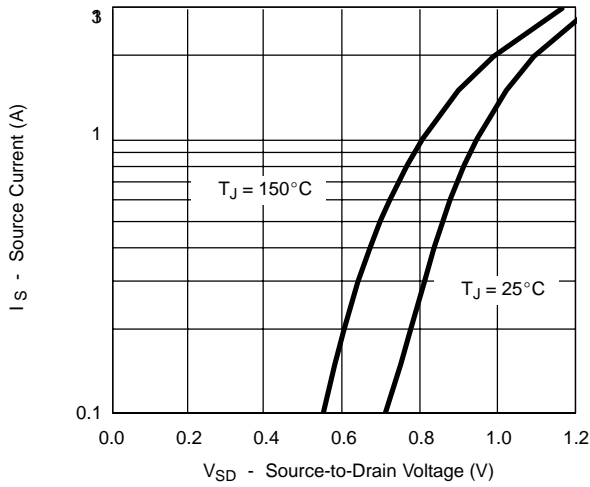




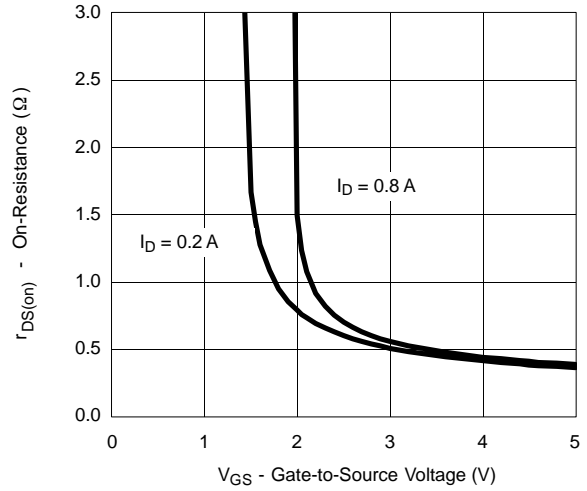
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P-CHANNEL

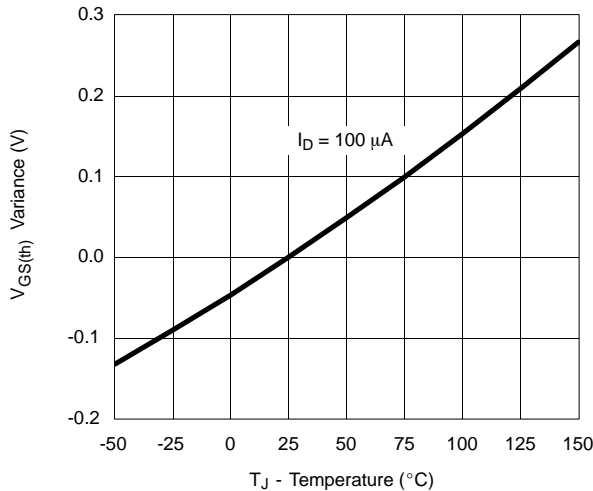
Source-Drain Diode Forward Voltage



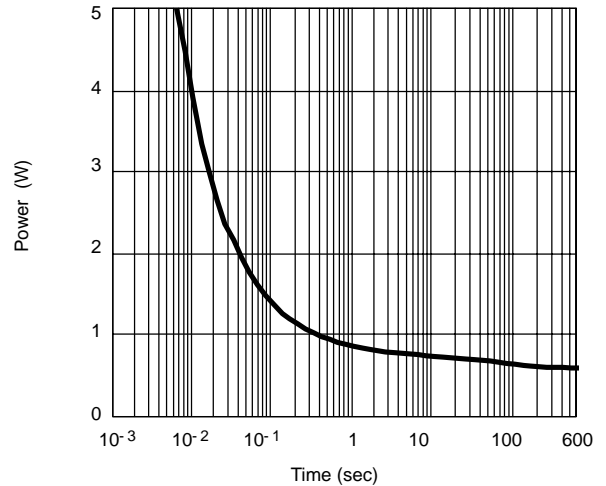
On-Resistance vs. Gate-to-Source Voltage



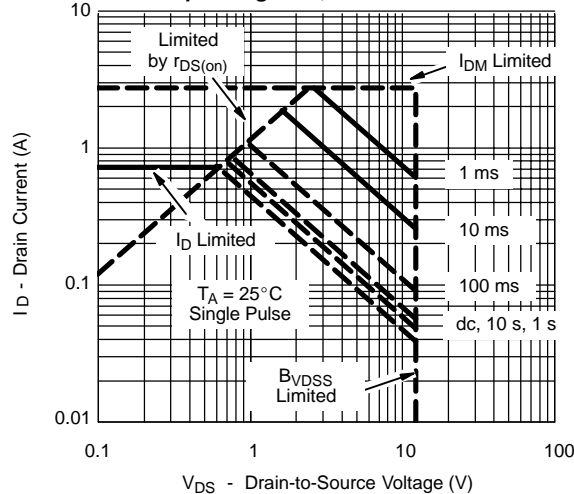
Threshold Voltage



Single Pulse Power



Safe Operating Area, Junction-to-Ambient

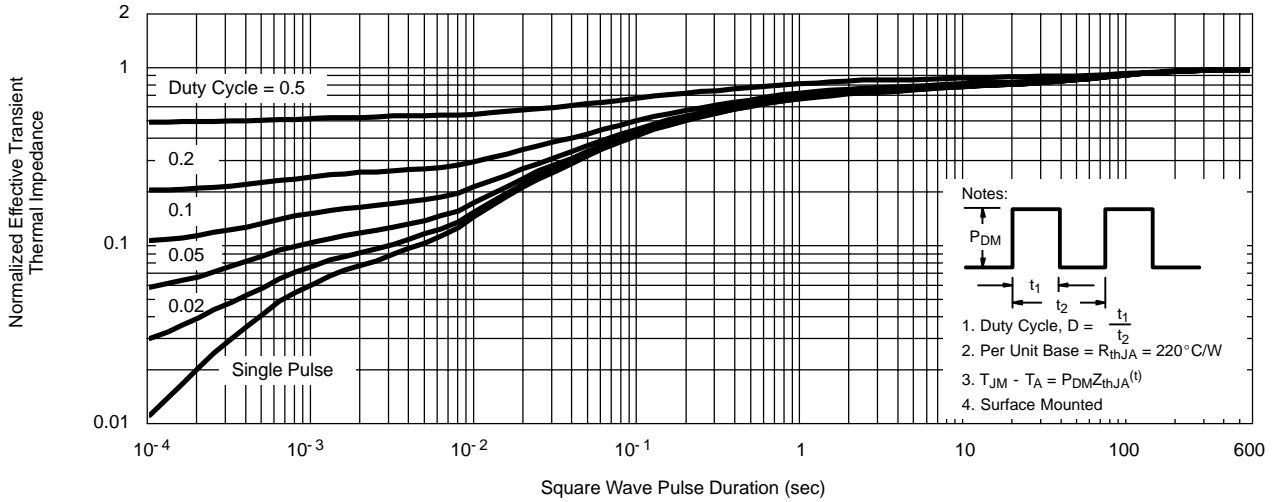




TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

P-CHANNEL

Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

